

**Amendments to the Claims**

This listing of claims will replace all prior versions, and listings, of claims in the application:

**Listing of the Claims**

1-20 (canceled without prejudice).

21. (currently amended) A power supply, comprising:  
first and second switch-mode power supply units;  
both said first and second switch-mode power supply units operate during normal operation of said power supply;  
said first switch-mode power supply unit is switched off responsive to a control voltage during standby operation of said power supply;  
a first driver stage in said first switch-mode power supply unit, said first driver stage having a first output having a higher voltage during said normal operation than during said standby operation; and,  
said first output is coupled to an oscillator input of said second switch-mode power supply unit, said first output and the control voltage being used in standby operation for reducing the switching frequency of said second switch-mode power supply unit ~~during said standby operation~~.

22. (currently amended) The power supply as claimed in claim 21, wherein the control voltage is transmitted via an optocoupler together with a ~~the~~ regulating voltage for the first switch-mode power supply unit from the secondary side to the primary side.

23. (canceled without prejudice).

24. (previously presented) The power supply as claimed in claim 21, wherein:

the first driver stage is embodied in an integrated circuit and the first output of the first driver stage is the output of an error amplifier in said integrated circuit; and,

said first output is connected via a resistor to a capacitor of the oscillator of the second switch-mode power supply unit.

25. (previously presented) The power supply as claimed in claim 21, wherein:

a second driver stage forms part of said second switch-mode power supply unit; and,

a second output of the first driver stage of the first switch-mode power supply unit, which drives the switching element of the first switch-mode power supply unit, is coupled by a series circuit, having a current limiter and a rectifier, to an oscillator input of the second driver stage of the second switch-mode power supply unit for synchronizing the second switch-mode power supply unit.

26. (previously presented) The power supply as claimed in claim 25, wherein the second output of the first driver stage of the first switch-mode power supply unit is connected by the series circuit to a capacitor of the oscillator of the second switch-mode power supply unit.

27. (previously presented) The power supply as claimed in claim 25, wherein an output of the second driver stage of the second switch-mode power supply unit is coupled by a switching stage to the series circuit for increasing a pulse width ratio of the second output of the first driver stage of the first switch-mode power supply unit.

28. (previously presented) The power supply as claimed in claim 27, wherein the switching stage has a transistor, which turns off if the voltage of the output of the second driver stage turns off the switching transistor being connected downstream, and which is at low impedance on the output side if the output voltage of the second driver stage is high.

29. (previously presented) The power supply as claimed in claim 27, wherein the switching stage blocks signals of the series circuit if the output voltage of the second driver stage is high.

30. (previously presented) The power supply as claimed in claim 21, wherein the first and second switch-mode power supply units in each case have a transformer having a primary winding and at least one secondary winding, a switching element coupled to one of the primary windings, and a driver stage, and in that both switch-mode power supply units operate according to the flyback converter principle.

31. (previously presented) The power supply as claimed in claim 21, wherein the control voltage is coupled to a control input of the first switch-mode power supply unit and an oscillator input of the second switch-mode power supply unit.